

Amendments to the Specification

Page 14, paragraph starting with line 13:

The data mapping circuit 11 and the mapping table 12 (see Fig. 1) are designed to compensate for such a timing difference (or a phase difference). Specifically, the mapping table 12 contains predetermined data mapping information predetermined in response to an estimated timing difference between the real-part components and the imaginary-part components of the modulation-resultant digital signal generated by the digital quadrature modulator 15 in the absence of correction or compensation. The data mapping circuit 11 accesses the mapping table 12, and converts the input digital data into the digital signals "i" and the digital signals "q" by referring to the contents of the mapping table 12 (that is, the data mapping information in the mapping table 12). The digital signal "i" and the digital signal "q" provide compensation for the actual phase difference between the real-part components and the imaginary-part components of the modulation-resultant digital signal generated by the digital quadrature modulator 15. The data mapping circuit 11 feeds the digital signal "i" and the digital signal "q" to the IFFT device 13.

Page 25, line 20:

$$\text{Im}(-\omega_n) = (1/2)BA\{\sin\beta - \sin\alpha + \sin((\beta-\gamma) + \sin(\alpha+\gamma) \cdots (21)$$